**Sequential Logic Design with Counters in Verilog Design Report**

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***Introduction:***

This experiment seeks to act as an introduction to sequential logic design and

**GCD Algorithm:**

The steps of the algorithm are as follows:

1. Start with two positive numbers, A and B
2. If min(A, B) == 0, jump to step 5
3. Subtract the smaller number from the larger number
4. Jump to step 2
5. The maximum of A and B is the GCD

Algorithm given A=55, B=121